AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Serial Number: 10/608051 Filing Date: June 27, 2003

Title: FREQUENCY PRESCALER APPARATUS, METHOD, AND SYSTEM

Assignee: Intel Corporation

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IN THE ABSTRACT OF THE DISCLOSURE

Please cancel the previous Abstract and replace it with the following:

A frequency prescaler includes an asynchronous counter having a least significant stage clocked by an input signal, and a first true single phase clock flip-flop having an input stage with an embedded logic gate to decode a state of the asynchronous counter, configured to modify a modulus of the asynchronous counter.